

AMENDMENTS TO THE CLAIMS

1. (Previously Presented) An electronic system comprising:

a host controller configured to execute an operating system and one or more management agents;

a plurality of components configured to provide functionality for the electronic system;
and

a margin testing system integrated with said host controller and said plurality of components, said testing system configured for margin testing one or more of said plurality of components, said margin testing system comprising:

a baseboard management controller (BMC) configured to communicate with said host controller; and

a digital parameter adjuster configured to communicate with said BMC and configured to set at least one operating parameter associated with at least one of said plurality of components to one or more test values in response to commands from said BMC,

wherein said BMC is configured to monitor a response of the electronic system to said one or more test values.

2. (Previously Presented) The electronic system of claim 1, further comprising:

a hardware monitor configured to communicate with said BMC and at least one of said plurality of components to receive information from said at least one of said plurality of components in response to said one or more test values and to transmit said received information to said BMC.

3. (Previously Presented) The electronic system of claim 1, further comprising:

a diagnostics software configured to collect data regarding said response of the electronic system to said one or more test values.

4. (Previously Presented) The electronic system of claim 3, wherein said BMC is configured to execute said diagnostics software.
5. (Previously Presented) The electronic system of claim 1, wherein said BMC is further configured to transmit one or more command signals to said parameter adjuster to effect variation of said at least one operating parameter.
6. (Previously Presented) The electronic system of claim 1, wherein said at least one operating parameter comprises a frequency at which a clock signal is applied to said at least one of said plurality of components.
7. (Previously Presented) The electronic system of claim 2, further comprising:
at least one communications bus configured to couple said BMC to said parameter adjuster and said hardware monitor.
8. (Previously Presented) The electronic system of claim 1, wherein said BMC is configured to implement management of said plurality of components of the electronic system.
9. (Cancelled)
10. (Currently Amended) The electronic system of claim 1, wherein said BMC is configured to implement Intelligent Platform Management Interface (IPMI) protocol.
11. (Currently Amended) The electronic system of claim 1, further comprising:
an I²C-based Inter-Integrated Circuit-based bus configured to provide communication between said BMC and said parameter adjuster.
12. (Currently Amended) The electronic system of claim 11, wherein said I²C-based Inter-Integrated Circuit-based bus comprises an Intelligent Platform Management Bus (IPMB).

13. (Previously Presented) The electronic system of claim 1, wherein said parameter adjuster comprises a digital programmable frequency synthesizer.

14. (Previously Presented) The electronic system of claim 13, wherein said frequency synthesizer is configured to receive an input reference clock signal and, in response to a command signal from said BMC, generates an output clock signal as a multiple of said input clock signal.

15. (Previously Presented) The electronic system of claim 14, wherein said frequency synthesizer is configured to apply said output clock signal to one or more of said plurality of components for testing thereof.

16. (Previously Presented) The electronic system of claim 1, wherein the electronic system comprises a computer system.

17. (Previously Presented) The electronic system of claim 16, wherein said computer system comprises a computer server.

18-75. (Cancelled)